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| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br>( Not for submission under 37 CFR 1.99) | Application Number     |                       | 10522262   |
|   | Filing Date            |                       | 2005-09-21 |
|   | First Named Inventor   | Iiro Hietanen         |            |
|   | Art Unit               | 2815                  |            |
|   | Examiner Name          | Michael B. Shingleton |            |
|   | Attorney Docket Number | 800186US              |            |

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|   |  |                          |
|---|--|--------------------------|
| 1 | E. M. CHOW ET AL., "Through-Wafer Electrical Interconnects Compatible With Standard Semiconductor Processing", Solid-State Sensor and Actuator Workshop, June 4-8, 2000, pages 343-346, Hilton Head Island, South Carolina | <input type="checkbox"/> |
| 2 | A.J. FROST, Opponent's Submissions for European Patent No. 1525623 filed in European Patent Office, Munich, August 10, 2010, pages 1-26  | <input type="checkbox"/> |

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